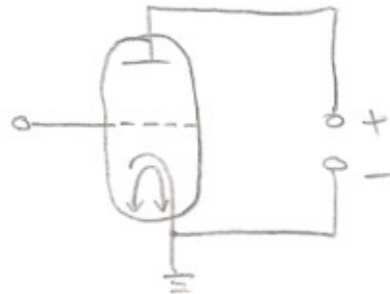
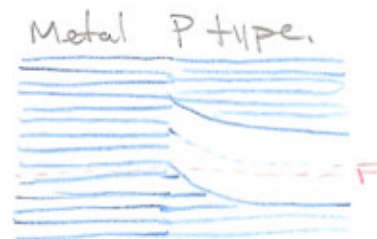
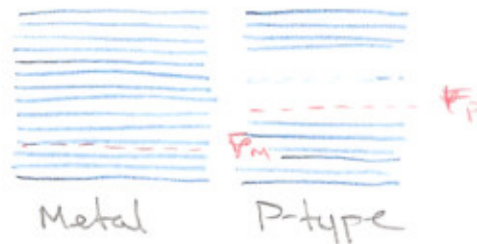


MOS (Metal oxide) TRANSISTORS

Vacuum Triode

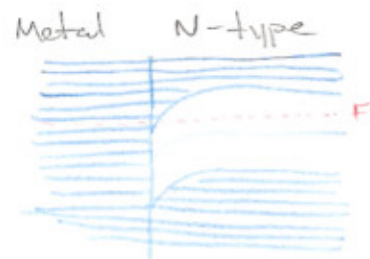


note: we create charges in the vacuum so to adjust the overall conductivity



here it becomes more P.

And similiarly for n-type.

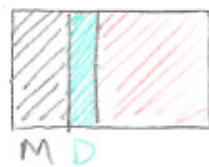


$$\phi_m < \phi_n$$

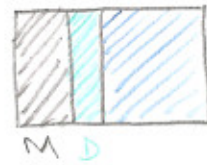
$$\Delta E = F_p - F_m$$

$$\Delta E = F_m - F_n$$

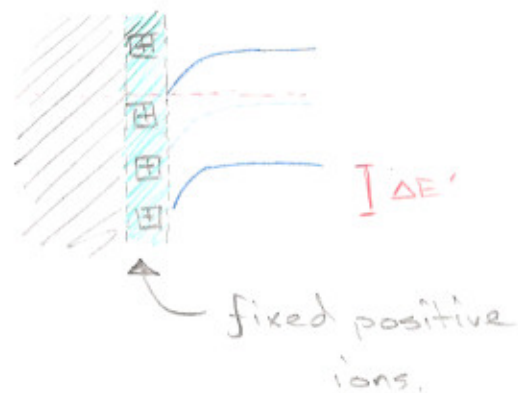
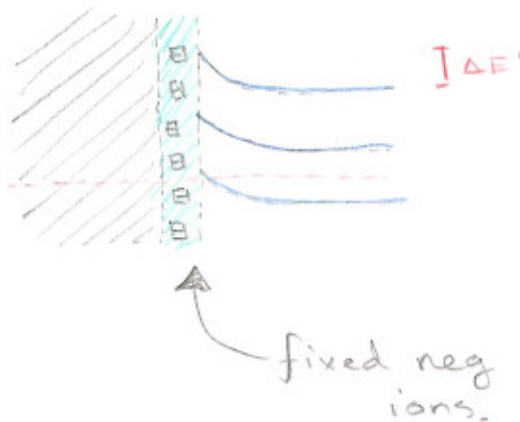
Now suppose that between the metal and semiconductor, some dielectric is inserted.



P TYPE



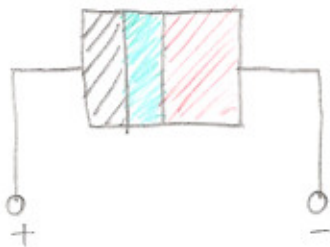
N TYPE



note: this will result in more curve ($\Delta E' > \Delta E$)

$$\Delta E'' = \Delta E' - \Delta E$$

We wish to adjust the conductivity by applying an external voltage source to the dielectric.



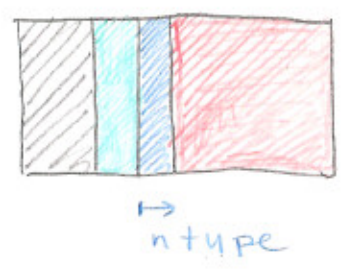
note: As more voltage is applied the curve ref P type will slope down until the middle of energy band gap reaches the Fermi level.

$$V' = \frac{\Delta E'}{q}$$

If the voltage is i/c more, then the curve will continue to curve down, n-type conductivity is achieved.

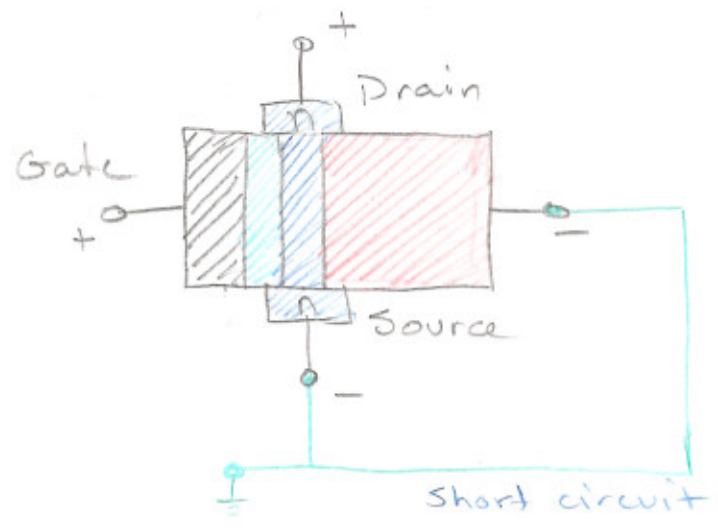


Hence, by applying voltage we have achieved different conductivity.



note = by applying voltages we can adjust the width of the ntype material.

For the Ntype device, we switch the polarity of the voltage applied, And a similar effect occurs,



Again the N-type device is designed in a similar fashion, but the polarity of the Drain to Source voltage is reversed.